

10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

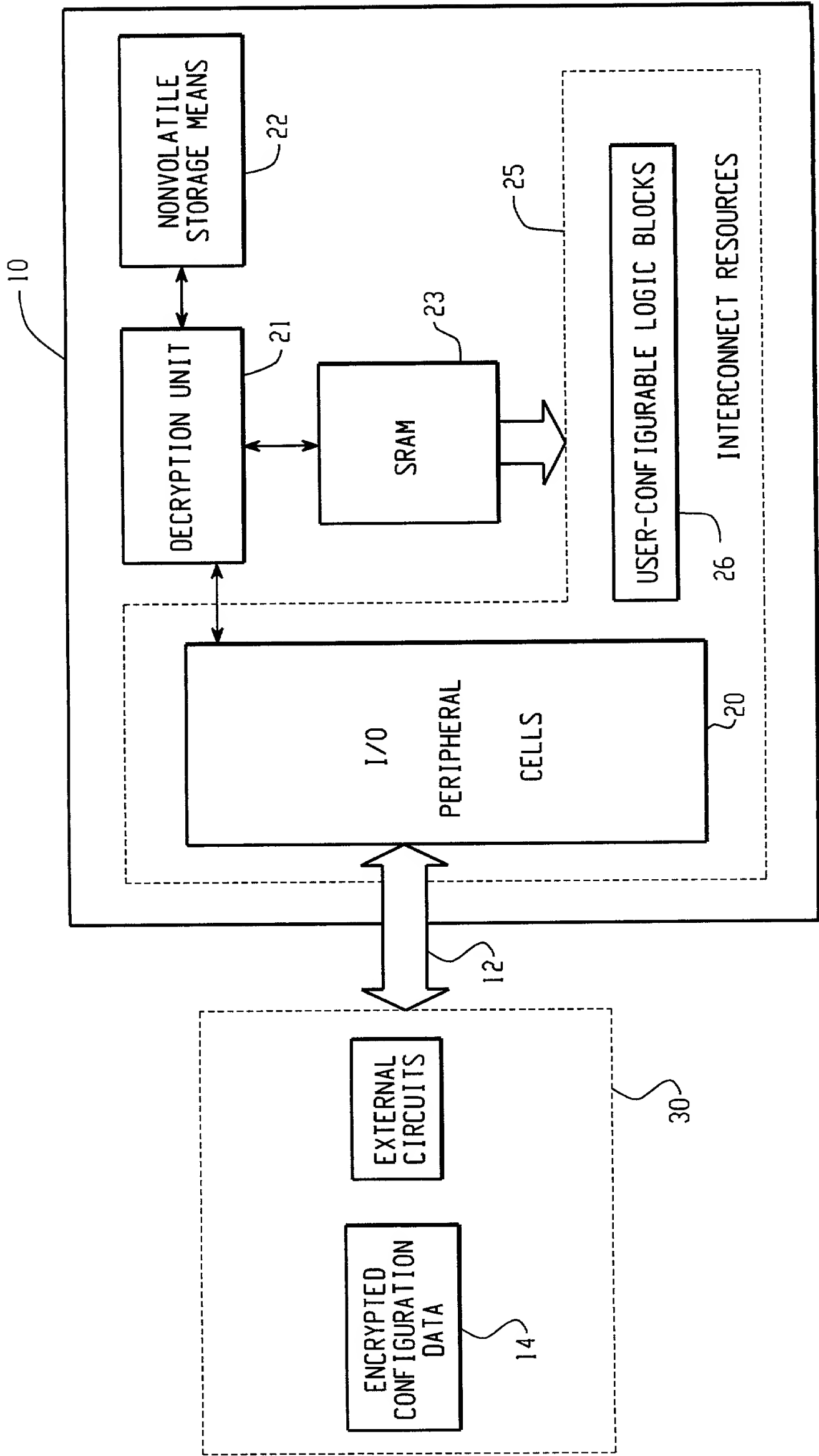


Fig. 1

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42	POSITION / ADDRESS																				
44	ROW (1)	1	1	1	0	1	0	1	1	1	0	0	0	1	0	1	1	1	0	0	1
46	ROW (2)	1	1	1	0	1	0	0	1	1	0	0	0	1	1	0	0	1	0	0	1
48	ROW (3)	0	0	0	0	0	0	1	0	0	0	0	0	0	1	1	1	0	0	0	0

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KEYS:  
ROW (1): ORIGINAL CONFIGURATION-DATA SEQUENCE  
ROW (2): ENCRYPTED CONFIGURATION-DATA SEQUENCE  
ROW (3): SECRET SEQUENCE STORED IN NONVOLATILE STORAGE MEANS 22  
POSITIONS 7 AND 14-16: BITS TO BE ENCRYPTED

Fig. 2

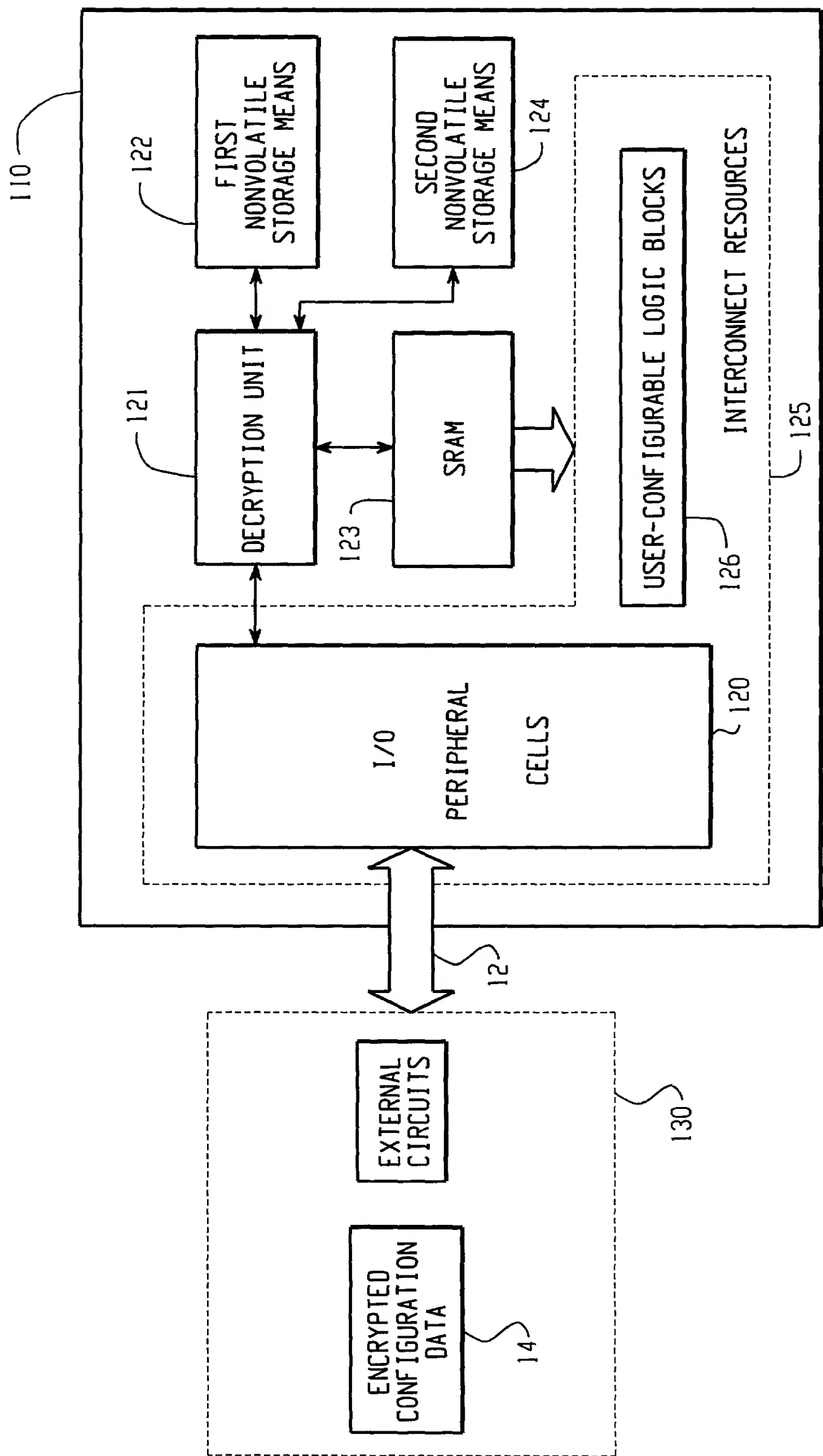
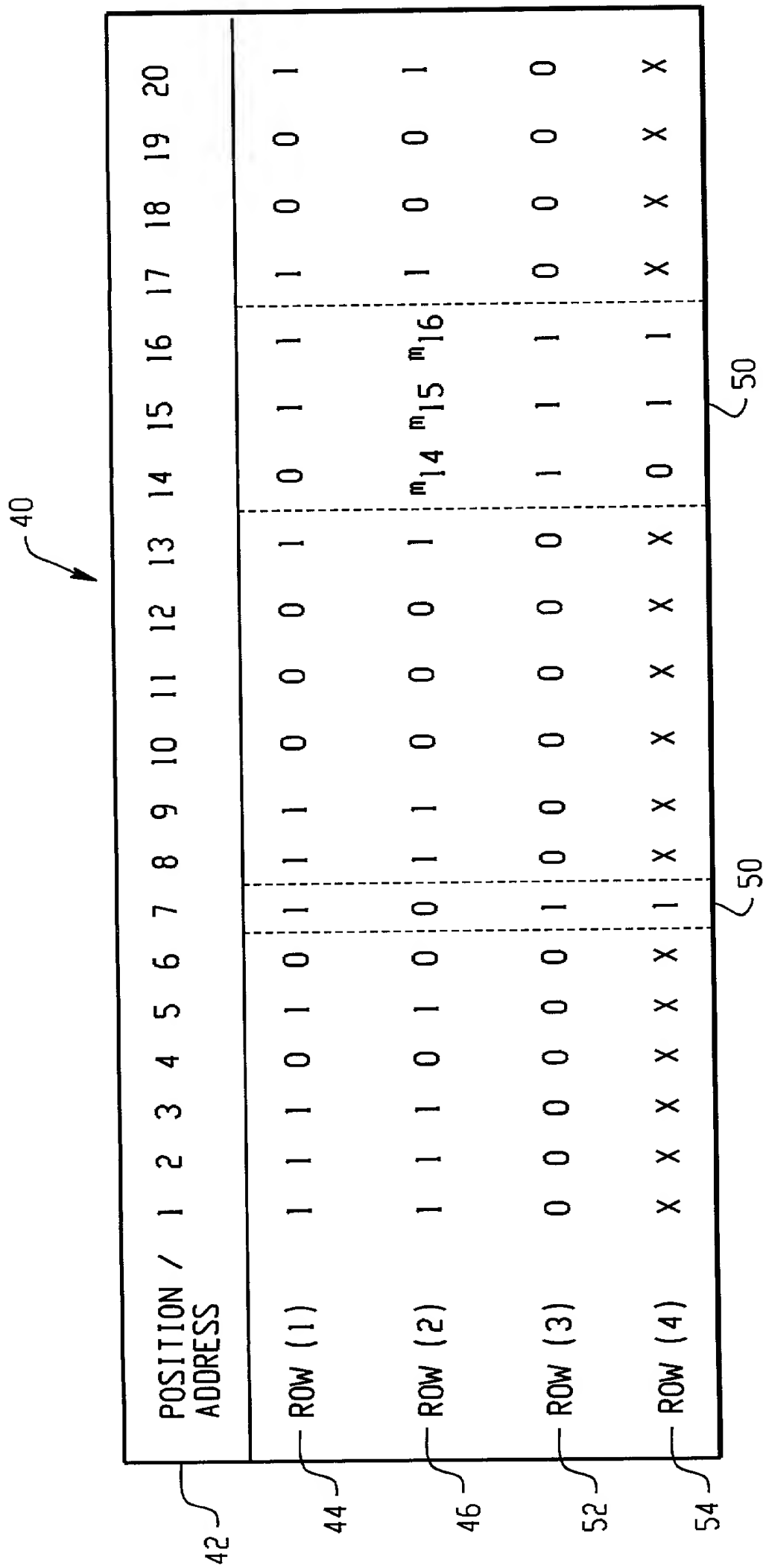


Fig. 3



KEYS:

ROW (1): ORIGINAL CONFIGURATION-DATA SEQUENCE

ROW (2): ENCRYPTED CONFIGURATION-DATA SEQUENCE:  $m_{14} m_{15} m_{16} \in \{000, 001, 010, 100, 101, 110, 111\}$

ROW (3): ADDRESS INFORMATION STORED IN THE FIRST NONVOLATILE STORAGE MEANS 122

ROW (4): OVERWRITE-DATA INFORMATION STORED IN THE SECOND NONVOLATILE STORAGE MEANS 124; X=DON'T CARE.

POSITIONS 7 AND 14-16: BITS TO BE ENCRYPTED

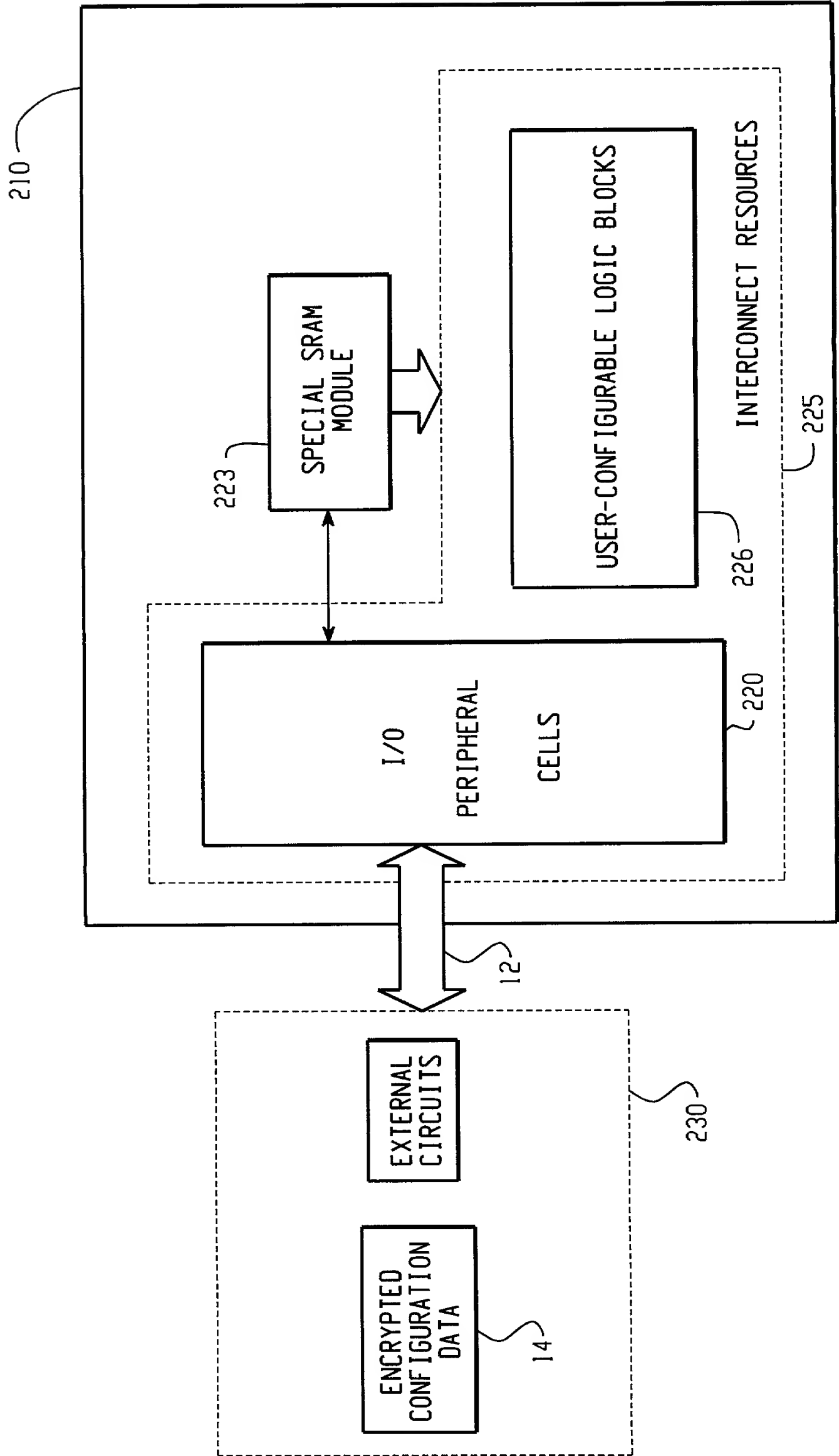


Fig. 5

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POSITION/ ADDRESS	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
ROW (1)	1	1	1	0	1	0	1	1	1	0	0	0	1	0	1	1	1	0	0	1
ROW (1)	1	1	1	0	1	0	0	1	1	0	0	0	1	$m_{14}$	$m_{15}$	$m_{16}$	1	0	0	1
ROW (1)	R/W	R/W	R/W	R/W	R/W	R/W	@1	R/W	R/W	R/W	R/W	R/W	R/W	@0	@1	@1	R/W	R/W	R/W	R/W

KEYS:  
ROW (1): ORIGINAL CONFIGURATION-DATA SEQUENCE  
ROW (2): ENCRYPTED CONFIGURATION-DATA SEQUENCE;  $m_{14} m_{15} m_{16} \in \{000, 001, 010, 100, 101, 110, 111\}$   
ROW (4): READ/WRITE STATUS OF THE SPECIAL SRAM MODULE 223; R/W=READ-AND-WRITE POSSIBLE; @=STICKING AT POSITIONS 7 AND 14-16: BITS TO BE ENCRYPTED

Fig. 6

22 →

7	60
14	60
15	60
16	
EOL	62
:	
:	
:	
:	

Fig. 7

22 →

(7, 1)	60
(14, 0)	60
(15, 1)	60
(16, 1)	
EOL	62
:	
:	
:	
:	

Fig. 8

22 →

(7, MODIFICATION, x)	60
(14, OVERWRITE, 0)	60
(15, MODIFICATION, x)	60
(16, OVERWRITE, 1)	
EOL	62
:	
:	
:	
:	

Fig. 9